



PATENT

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Alexei Sadovnikov

Application No.: 10/669,045

Filed: September 23, 2003

For: LOW-DOPED BASE SPACER FOR
REDUCTION OF THE EMITTER-
BASE CAPACITANCE IN BIPOLAR
TRANSISTORS WITH SELECTIVITY
GROWN EPITAXIAL BASE

Confirmation No.: Unknown

Group Art Unit: Unknown

Examiner: Unknown

SUBMISSION OF FORMAL DRAWINGS

121 Spear Street, Suite 290
San Francisco, CA 94105
(415) 512-1312

Mail Stop PGPUB Drawings
Commissioner for Patents
Attn: Official Draftsperson
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited
with the United States Postal Service as First Class Mail in an
envelope, addressed to: Commissioner for Patents, P.O. Box
1450, Alexandria, VA 22313-1450 on October 14, 2003.

STALLMAN & POLLOCK LLP

Dated: 10/14/03

By: 

Lana T. Brenner

Sir:

Enclosed are three (3) sheets of formal drawings for filing in the above-referenced
application.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: 10-13-03

By: 

Michael J. Pollock
Reg. No. 29,098

Attorneys for Applicant(s)